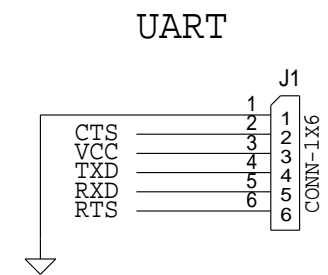
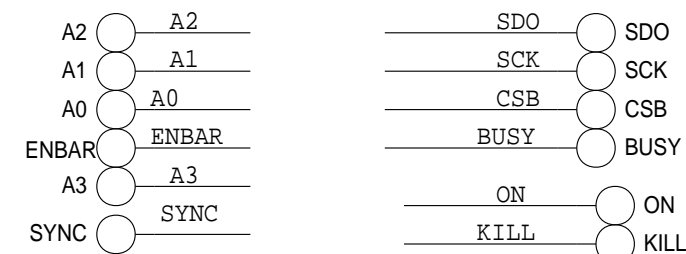
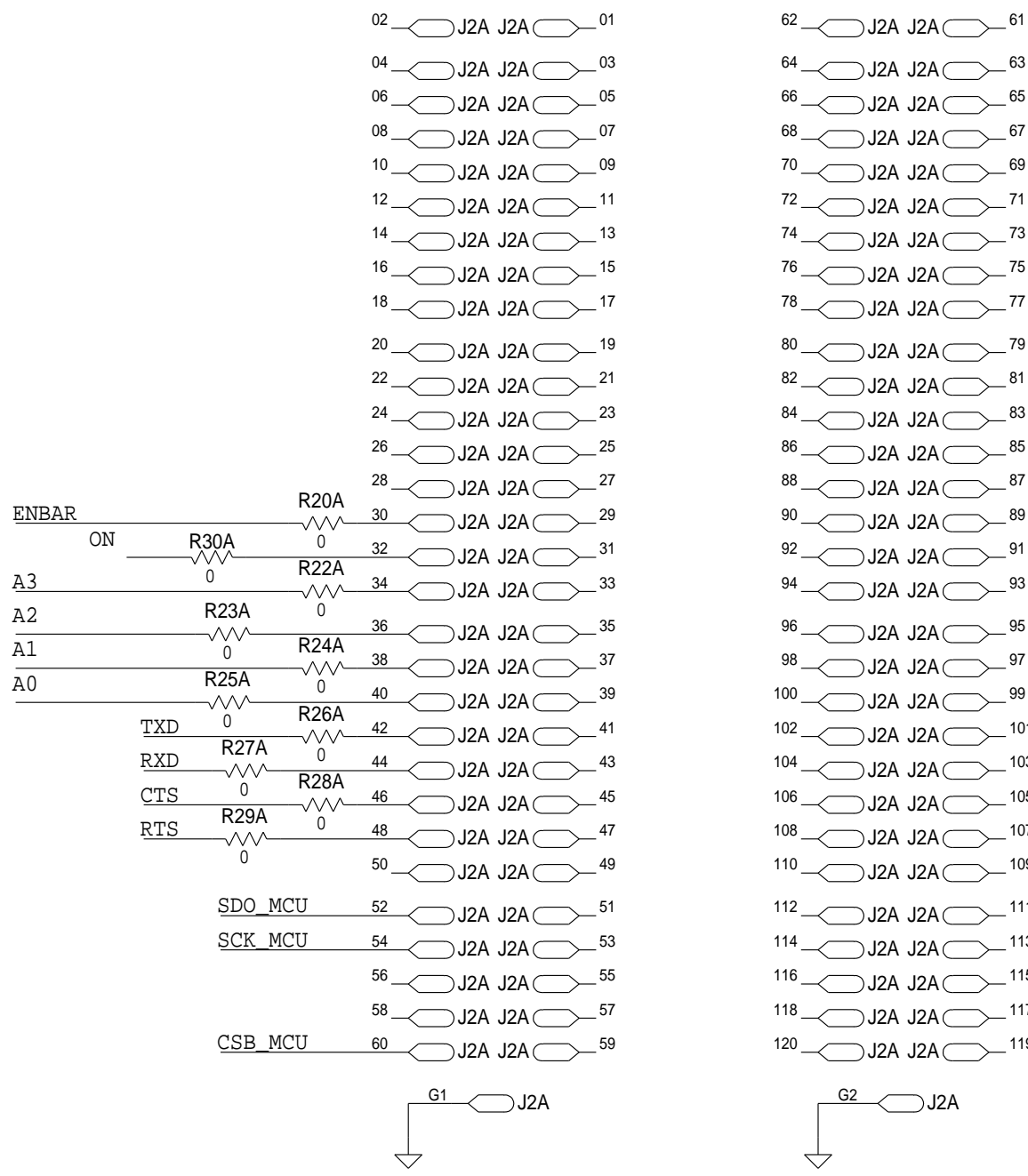
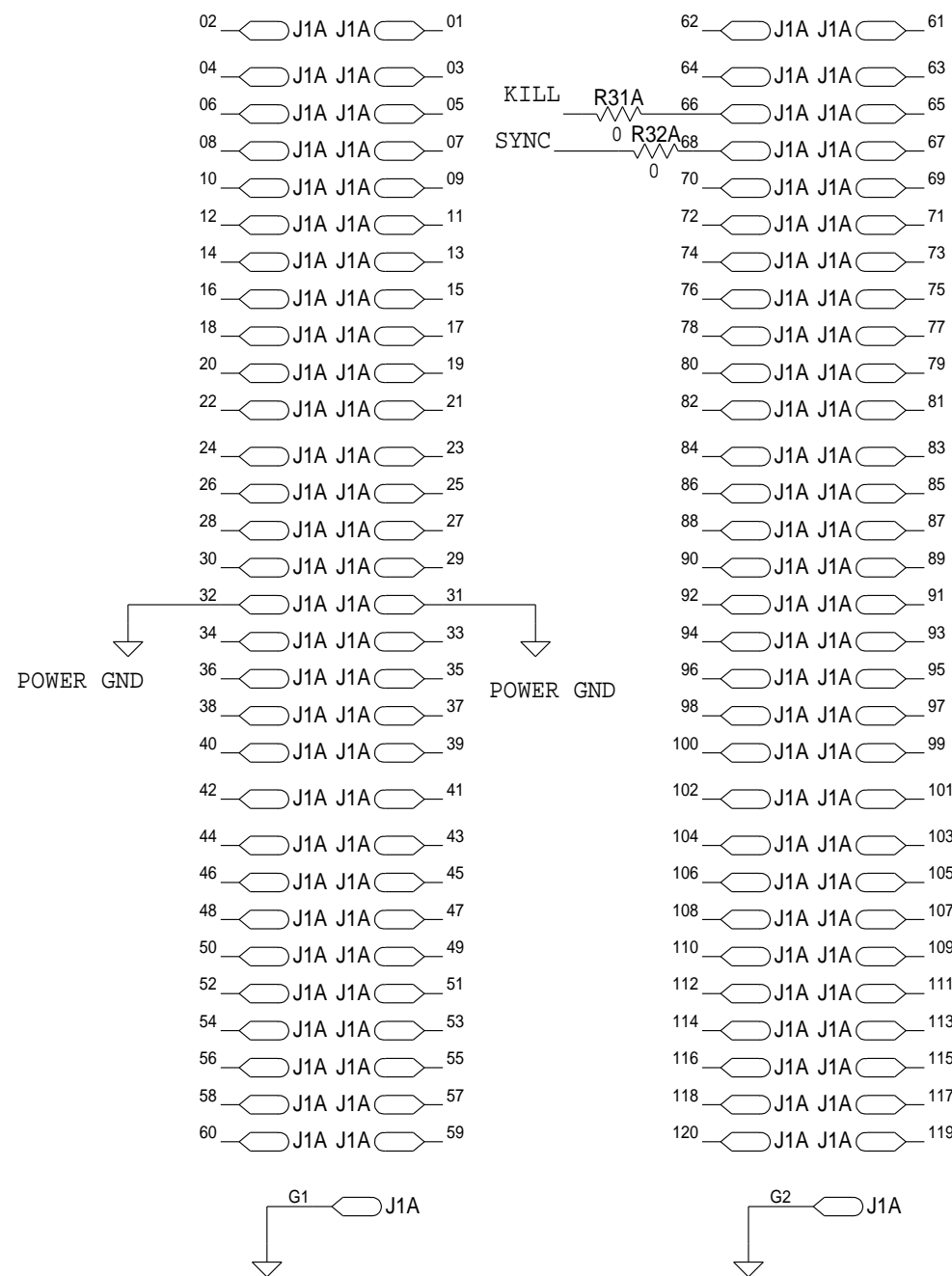


BOARD CONNECTORS



ADC SPI JUMPER SELECTIONS



DRAWN BY: ALAN ERZINGER	DATE: 11/29/2022	ENGINEER: JENNY BRIK	DATE:
RELEASED BY:	DATE:	TITLE: FPGA POWER BOARD DEMO	
UPDATED BY: JENNY BRIK	DATE: 1/30/2023		
		TESTER	
		MASK#	HRDWR ID
FILENAME: ISL73141VMREFEV1Z		SHEET 2 OF 2	